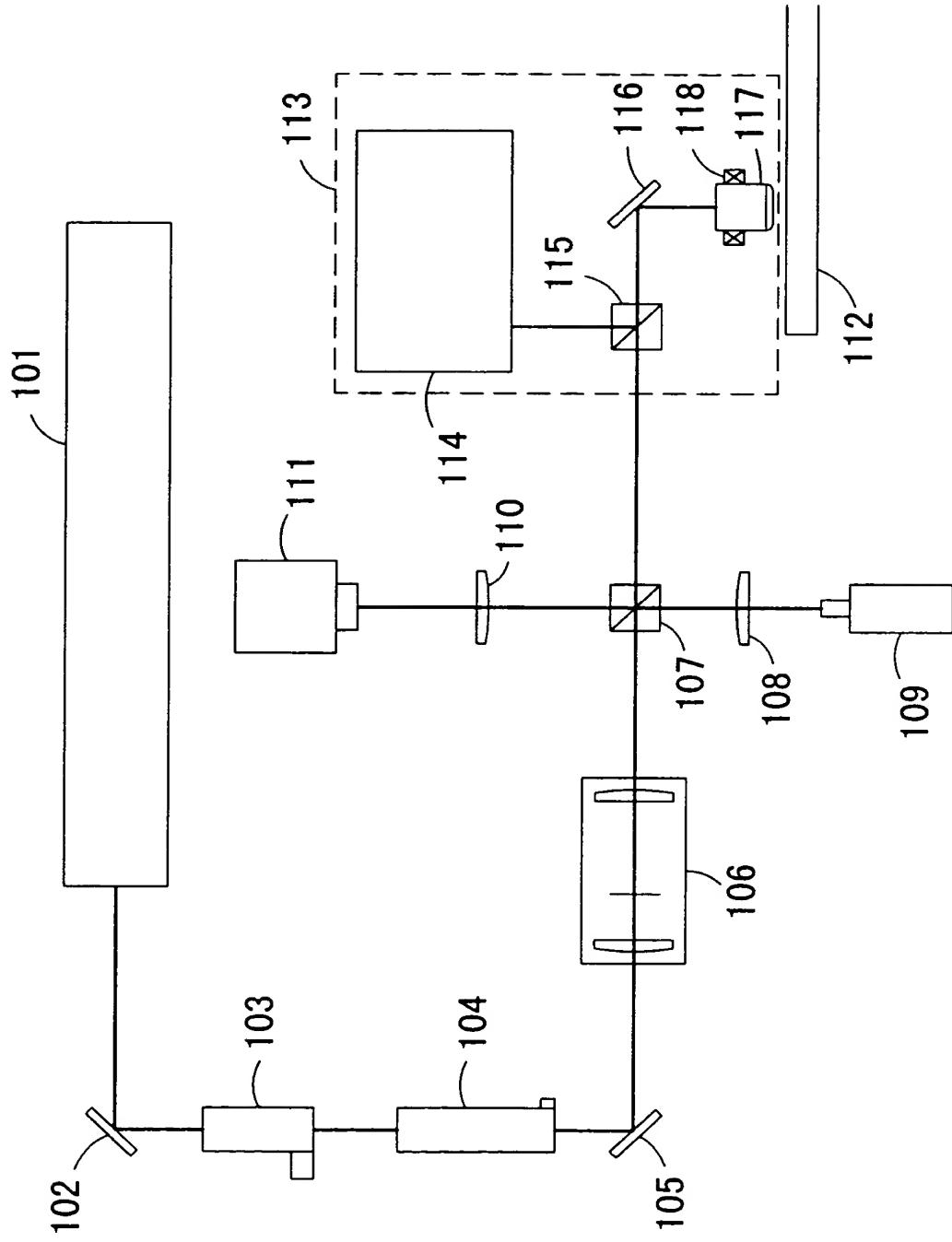


FIG. 1



7=

FIG. 2

## Control Position Correcting Circuit

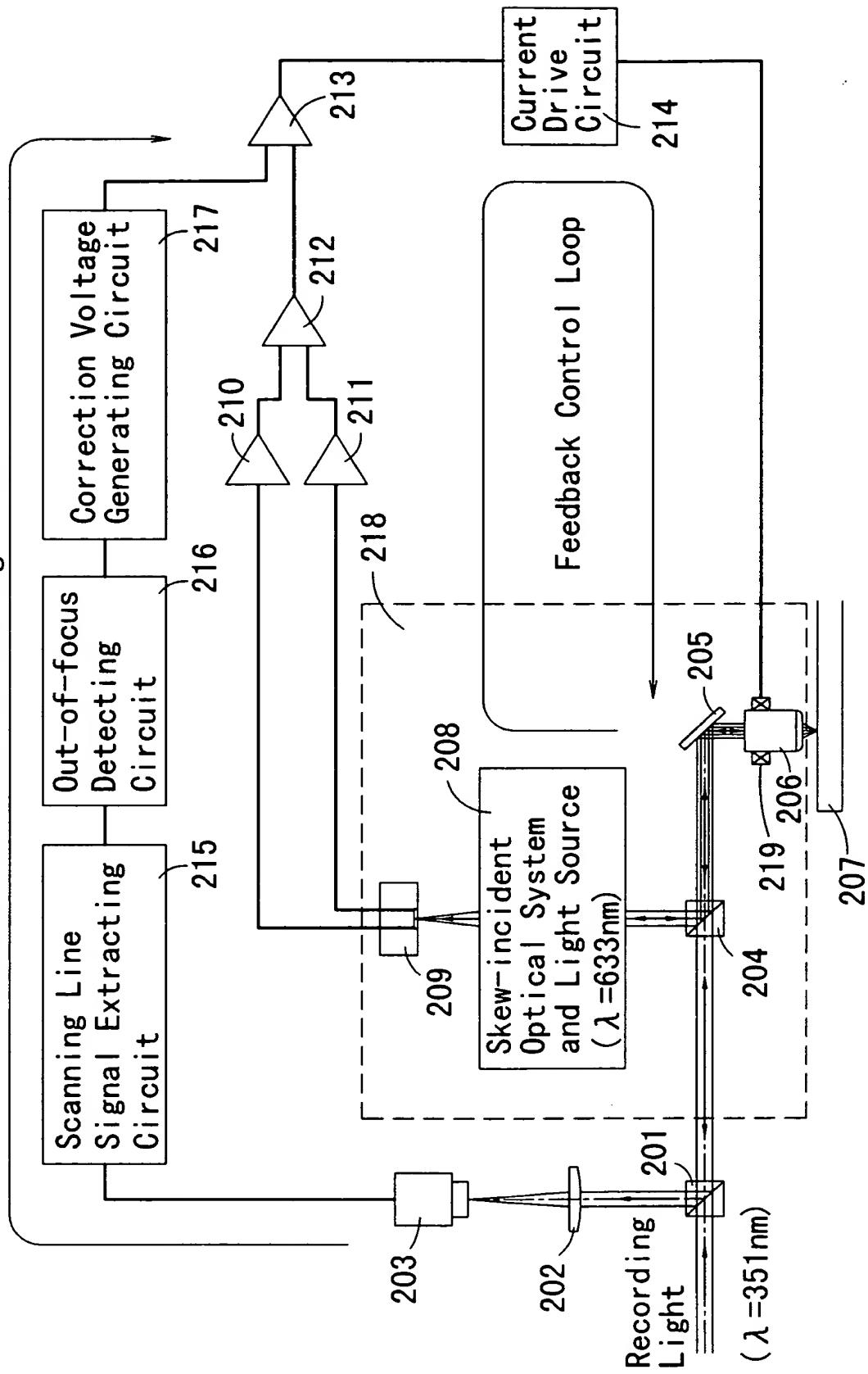
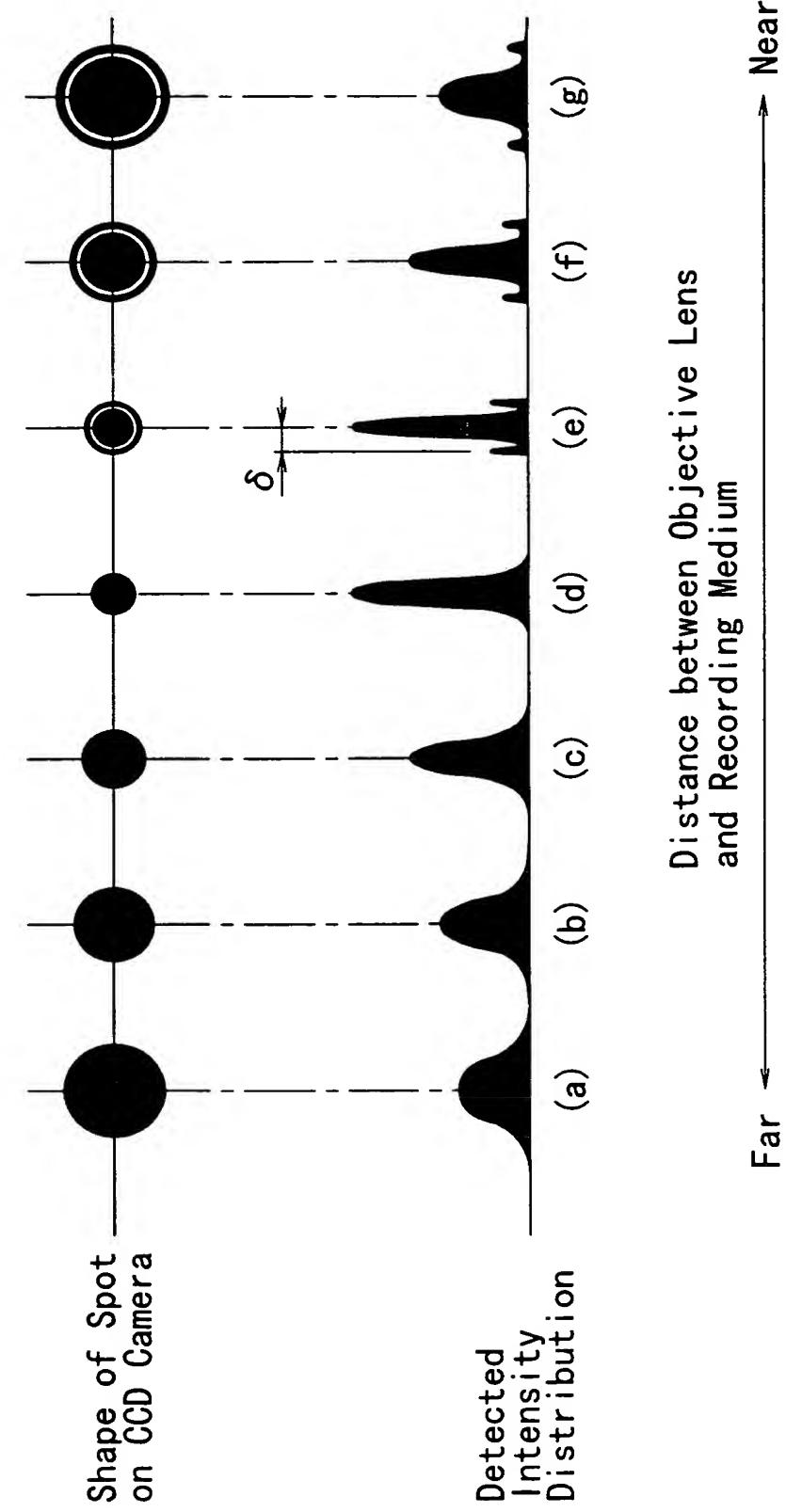
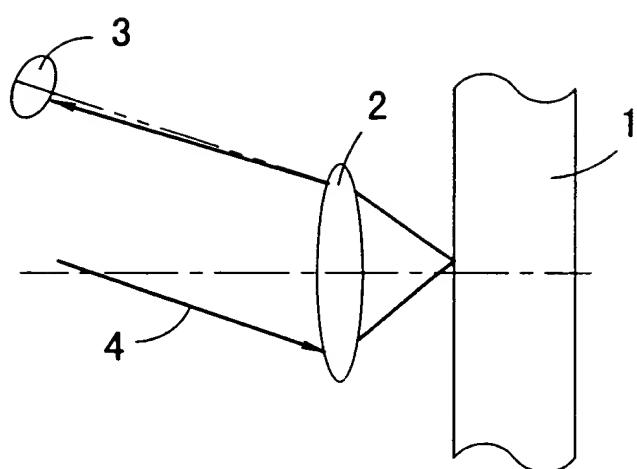


FIG. 3

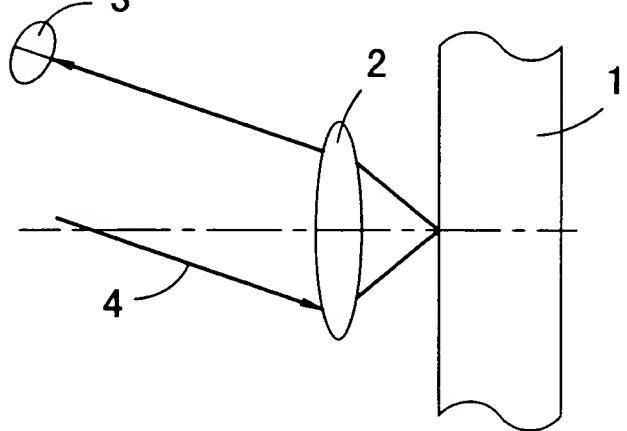


F I G. 4

(a)



(b)



(c)

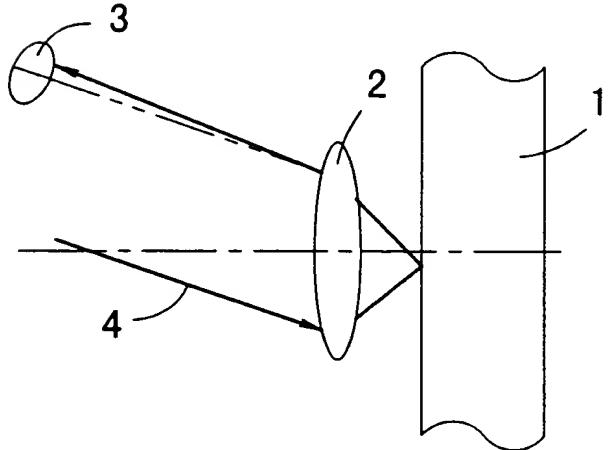
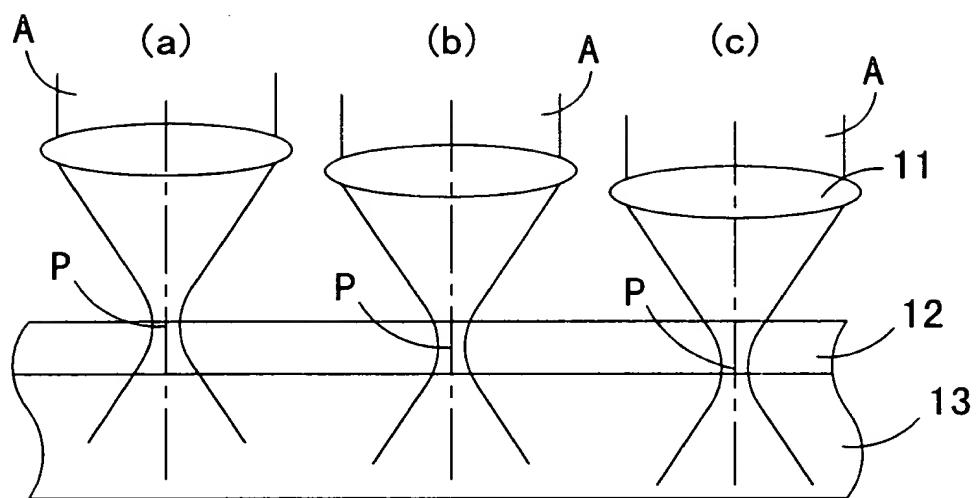


FIG. 5



↓ Development

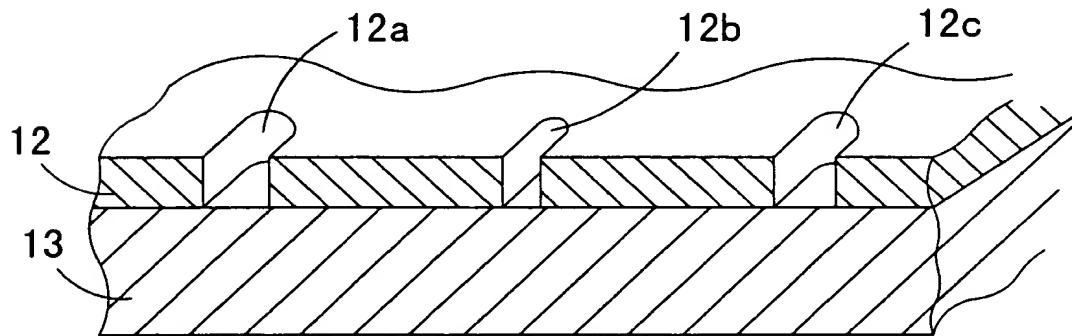
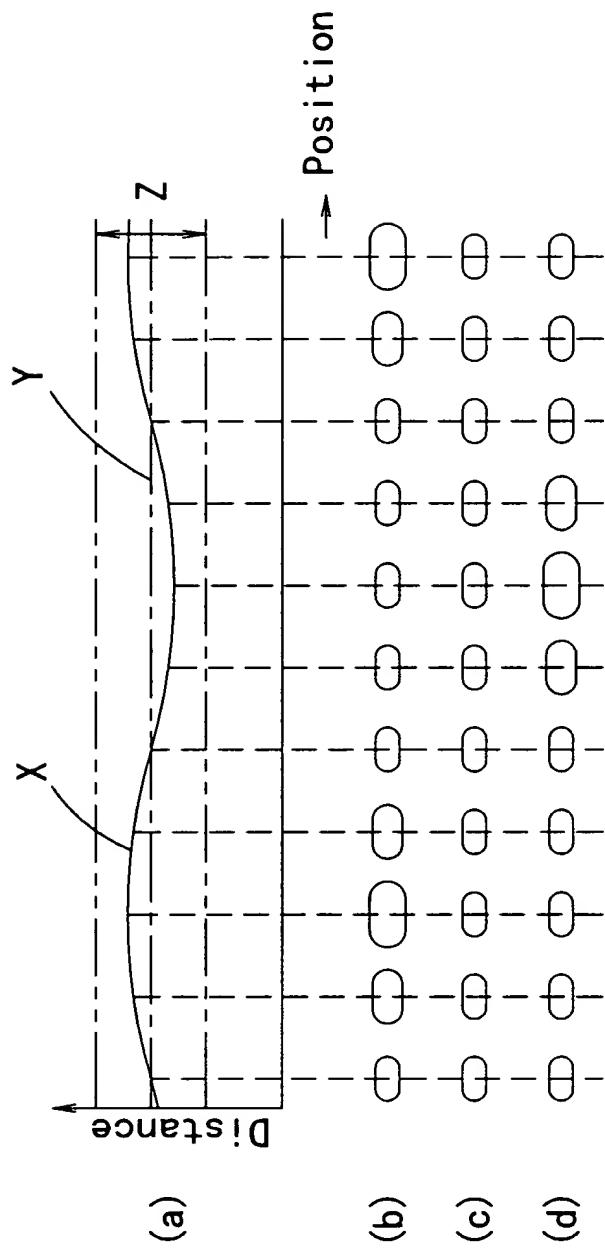


FIG. 6



70f1

## FIG. 7

